REMARKS

Claims 1-20 are pending in this application. Claims 1-3, 5, 7-9, 11-13, 15-16 and 18-20 have been rejected under 35 U.S.C. § 102(b) as being anticipated by "Efficient Algorithms for Sorting and synchronization" by Andrew Tridgell. The Applicant respectfully requests the Examiner to withdraw the finality of this Office Action in order to more fully address the Applicant's argument presented in the Response submitted on June 26, 2006.

In particular, the Applicant argued that Tridgell does not teach or suggest "storing at least a portion of said target file checking data in a cache." In the September 29, 2006 Office Action, at page 8, the Examiner responds by saying,

> "Clearly, the prior art disclosed storing data in a cache. 'Data', which Applicant does not
> contest, is previously shown to be present in the Tridgell reference."

The Applicant does not dispute that a cache stores data, and that Tridgell discloses a cache. However, as set forth in the June 26, 2006 Response, claim 1(and also the other independent claims) requires more than just storing data in a cache. Claim 1 requires storing at least a portion of said target file checking data in a cache. The text of Tridgell that the Examiner cites merely provides general platform information used to generate results given in the Tridgell thesis.

Further, while Tridgell specifically describes how it uses cache, it does NOT teach or suggest using the cache to store target file checking data, as required by the independent claims. For example, at page 24, lines 10-19, and also at page 29, first full paragraph, Tridgell teaches using a cache to implement a sorting procedure. At page 93, lines 17-19, Tridgell describes how a web browser or a caching web server uses a cache system to store static web pages.

It is significant that Tridgell specifically teaches using cache for sorting algorithms and other purposes, but is silent on use of cache for the strong signature algorithm at section 3.2.4 that the Examiner cites with respect to claim 1.

Thus, while Tridgell merely describes the general use of cache as a CPU peripheral, and as a component of a sorting algorithm, Tridgell does not teach or suggest one of the novel aspects of the present independent claims, *i.e.,* storing at least a portion of said target file checking data in a cache (or similar limitations). Therefore the independent claims 1, 11, 15, 19 and 20 should be allowable. Since the remaining dependent claims depend from allowable
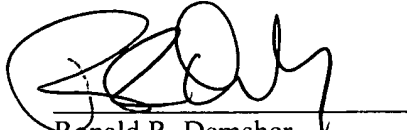
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claims, they should also be allowable. The Applicant respectfully requests reconsideration in light of the above remarks.

In view of the above remarks, applicant believes the pending application is in condition for allowance.

Respectfully submitted,

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